

PSE Conformance Test Suite

September 29 2025 7:30 PM

Port Count..... 1

Loop Count..... 1

PSE Tested: 000.000.000.000 Type-3

**Sifos®**
Technologies

Safety Index*: 100%

Error Log: None

802.3bt 4Pr Conformance Report

version 5.3.25

report version 5.3.07

PSE Type: 3 MDI-X+MDI

100%

Interop Index*: 100%

100%

Chassis ID: 192.168.221.105

TestLoop: 1

PSA-3000 Ports

1-1

UNITS

Min

Max

Average

Low Limit

P/F

High Limit

P/F

Test: det_v

Open Circuit Voc A= 9.2 volts

9.2

9.2

9.2

0

Pass

30

Pass

Open Circuit Voc B= 9.1 volts

9.1

9.1

9.1

0

Pass

30

Pass

Backoff Voltage A= 0.5 volts

0.5

0.5

0.5

0

Pass

2.8

Pass

Backoff Voltage B= 0.5 volts

0.5

0.5

0.5

0

Pass

2.8

Pass

Backoff Voltage Ss= 0.5 volts

0.5

0.5

0.5

0

Pass

2.8

Pass

Max Det Step V A= 9.02 volts

9.02

9.02

9.02

3.8

Pass

10

Pass

Max Det Step V B= 8.93 volts

8.93

8.93

8.93

3.8

Pass

10

Pass

Min Det Step V A= 4.96 volts

4.96

4.96

4.96

2.8

Pass

9

Pass

Min Det Step V B= 4.75 volts

4.75

4.75

4.75

2.8

Pass

9

Pass

Det Step Changes A= 3 ****

3

3

3

1

Pass

9

Pass

Det Step Changes B= 3 ****

3

3

3

1

Pass

9

Pass

Min Step DV A= 4.05 volts

4.05

4.05

4.05

1

Pass

7.2

Pass

Min Step DV B= 4.17 volts

4.17

4.17

4.17

1

Pass

7.2

Pass

Pre-Det CC Step V A= 5.2 volts

5.2

5.2

5.2

0

Pass

10

Pass

Pre-Det CC Step V B= 4.88 volts

4.88

4.88

4.88

0

Pass

10

Pass

Test: det_cc

Presumed CC DET SEQ= 1 ****

1

1

1

0

Pass

3

Pass

Conn Chk SS V A= 9.23 volts

9.23

9.23

9.23

2.8

Pass

10

Pass

Conn Chk SS V B= 9.06 volts

9.06

9.06

9.06

2.8

Pass

10

Pass

Conn Chk DS V A= 9.23 volts

9.23

9.23

9.23

2.8

Pass

10

Pass

Conn Chk DS V B= 9.06 volts

9.06

9.06

9.06

2.8

Pass

10

Pass

High Signature CC A= 1 ****

1

1

1

1

Pass

1

Pass

High Signature CC B= 1 ****

1

1

1

1

Pass

1

Pass

4Pair Start Fail= 0 ****

0

0

0

0

Pass

0

Pass

Test: det_i

Isc Init A= 1.24 mA

1.24

1.24

1.24

0

Pass

5

Pass

Isc Init B= 0.85 mA

0.85

0.85

0.85

0

Pass

5

Pass

Isc Det A= 1.24 mA

1.24

1.24

1.24

0

Pass

5

Pass

Isc Det B= 0.81 mA

0.81

0.81

0.81

0

Pass

5

Pass

Det Slew A= 0.0248 V/usec

0.0248

0.0248

0.0248

0

Pass

0.1

Pass

Det Slew B= 0.0162 V/usec

0.0162

0.0162

0.0162

0

Pass

0.1

Pass

Test: det_range

Rgood Max Single= 29 Kohm

29

29

29

27

Pass

32

Pass

Rgood Min Single= 17 Kohm

17

17

17

16

Pass

19

Pass

Cgood Max Single= 0.1 uF

0.1

0.1

0.1

0

Pass

10

Pass

Rgood Max Dual A= 29 Kohm

29

29

29

27

Pass

32

Pass

Rgood Max Dual B= 30 Kohm

30

30

30

27

Pass

32

Pass

Rgood Min Dual A= 17 Kohm

17

17

17

16

Pass

19

Pass

Rgood Min Dual B= 17 Kohm

17

17

17

16

Pass

19

Pass

Cgood Max Dual A= 0.1 uF

0.1

0.1

0.1

0

Pass

10

Pass

Cgood Max Dual B= 0.1 uF

0.1

0.1

0.1

0

Pass

10

Pass

Test: det_time

Detect Time Tdet A= 261.7 msec

261.7

261.7

261.7

0

Pass

500

Pass

Detect Time Tdet B= 261.7 msec

261.7

261.7

261.7

0

Pass

500

Pass

Backoff Time SS= 544.9 msec

544.9

544.9

544.9

15

Pass

9999

Pass

Backoff Time DS= 1007.8 msec

1007.8

1007.8

1007.8

15

Pass

9999

Pass

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1-1

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P/F

Class_Probe_SS=	0	****	0	0	0	0	Pass	1	Pass
EV_Count_7_SS=	4	Events	4	4	4	1	Pass	5	Pass
Long_EV1_Time_SS=	93.8	msec	93.8	93.8	93.8	88	Pass	105	Pass
Min_Class_EV_Time_SS=	9.8	msec	9.8	9.8	9.8	6	Pass	20	Pass
Max_Class_EV_Time_SS=	9.8	msec	9.8	9.8	9.8	6	Pass	20	Pass
Min_Mark_EV_Time_SS=	11.7	msec	11.7	11.7	11.7	6	Pass	12	Pass
Max_Mark_EV_Time_SS=	11.7	msec	11.7	11.7	11.7	6	Pass	12	Pass
Min_Mark_EV_LowI_SS=	11.7	msec	11.7	11.7	11.7	6	Pass	12	Pass
Final_Mark_EV_Time_SS=	11.7	msec	11.7	11.7	11.7	6	Pass	256	Pass
Cl_Prbr_Reset_Time_SS=	-1	****	-1	-1	-1	15	Pass	10000	Pass
Class_Probe_DA=	0	****	0	0	0	0	Pass	1	Pass
EV_Count_5D_DA=	3	Events	3	3	3	1	Pass	4	Pass
Long_EV1_Time_DA=	95.7	msec	95.7	95.7	95.7	88	Pass	105	Pass
Min_Class_EV_Time_DA=	9.7	msec	9.7	9.7	9.7	6	Pass	20	Pass
Max_Class_EV_Time_DA=	9.8	msec	9.8	9.8	9.8	6	Pass	20	Pass
Min_Mark_EV_Time_DA=	11.7	msec	11.7	11.7	11.7	6	Pass	12	Pass
Max_Mark_EV_Time_DA=	11.8	msec	11.8	11.8	11.8	6	Pass	12	Pass
Final_Mark_EV_Time_DA=	9.8	msec	9.8	9.8	9.8	6	Pass	256	Pass
Cl_Prbr_Reset_Time_DA=	-1	****	-1	-1	-1	15	Pass	10000	Pass
Class_Probe_DB=	0	****	0	0	0	0	Pass	1	Pass
EV_Count_5D_DB=	3	Events	3	3	3	1	Pass	4	Pass
Long_EV1_Time_DB=	95.7	msec	95.7	95.7	95.7	88	Pass	105	Pass
Min_Class_EV_Time_DB=	9.7	msec	9.7	9.7	9.7	6	Pass	20	Pass
Max_Class_EV_Time_DB=	11.7	msec	11.7	11.7	11.7	6	Pass	20	Pass
Min_Mark_EV_Time_DB=	9.8	msec	9.8	9.8	9.8	6	Pass	12	Pass
Max_Mark_EV_Time_DB=	11.8	msec	11.8	11.8	11.8	6	Pass	12	Pass
Final_Mark_EV_Time_DB=	9.8	msec	9.8	9.8	9.8	6	Pass	256	Pass
Cl_Prbr_Reset_Time_DB=	-1	****	-1	-1	-1	15	Pass	10000	Pass
Test: class_response									
Class_3_Count=	1	****	1	1	1	1	Pass	1	Pass
Class_4_Count=	3	****	3	3	3	1	Pass	3	Pass
Class_5_Count=	4	****	4	4	4	1	Pass	4	Pass
Class_6_Count=	4	****	4	4	4	1	Pass	4	Pass
Class_7_Count=	4	****	4	4	4	1	Pass	4	Pass
Class_8_Count=	4	****	4	4	4	1	Pass	4	Pass
Class_2D_Count_A=	1	****	1	1	1	1	Pass	3	Pass
Class_2D_Count_B=	1	****	1	1	1	1	Pass	3	Pass
Class_3D_Count_A=	1	****	1	1	1	1	Pass	3	Pass
Class_3D_Count_B=	1	****	1	1	1	1	Pass	3	Pass
Class_4D_Count_A=	3	****	3	3	3	1	Pass	3	Pass
Class_4D_Count_B=	3	****	3	3	3	1	Pass	3	Pass
Class_5D_Count_A=	3	****	3	3	3	1	Pass	3	Pass
Class_5D_Count_B=	3	****	3	3	3	1	Pass	3	Pass

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PSE Type: 3 MDI-X+MDI

Interop Index*: 100%

Chassis ID: 192.168.221.105

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PSA-3000 Ports

1-1

UNITS

Min

Max

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P/F

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P/F

Max SS Class=	6	****	6	6	6	3	Pass	6	Pass
Max DS Class=	4	****	4	4	4	1	Pass	4	Pass
Init Grant Match=	1	****	1	1	1	1	Pass	1	Pass
2-Pair Pairset=	0	****	0	0	0	0	Pass	2	Pass
PRI 4pr Pairset=	12	****	12	12	12	1	Pass	12	Pass
Test: class_err									
Class Ilim A=	70.5	mA	70.5	70.5	70.5	51	Pass	100	Pass
Class Ilim B=	69.7	mA	69.7	69.7	69.7	51	Pass	100	Pass
Pwr Cl 52 SS=	0	****	0	0	0	0	Pass	0	Pass
Pwr Cl 52 DSA=	0	****	0	0	0	0	Pass	0	Pass
Pwr Cl 52 DSB=	0	****	0	0	0	0	Pass	0	Pass
Class Reset SS=	1	****	1	1	1	1	Pass	1	Pass
Class Reset DS=	1	****	1	1	1	1	Pass	1	Pass
Mark Ilim A=	71	mA	71	71	71	5	Pass	100	Pass
Mark Ilim B=	71	mA	71	71	71	5	Pass	100	Pass
Inval Sig EV2 SS=	0	****	0	0	0	0	Pass	1	Pass
Inval Sig EV4 SS=	0	****	0	0	0	0	Pass	1	Pass
Inval Sig EV2 DSA=	0	****	0	0	0	0	Pass	1	Pass
Inval Sig EV2 DSB=	0	****	0	0	0	0	Pass	1	Pass
Test: pwrup_time									
Pwr On Time Tpon SS=	219.9	msec	219.9	219.9	219.9	144	Pass	400	Pass
Pwr On Time Tpon DSA=	155.5	msec	155.5	155.5	155.5	144	Pass	400	Pass
Pwr On Time Tpon DSB=	157.4	msec	157.4	157.4	157.4	144	Pass	400	Pass
Pwrup Rise Time A=	60	usec	60	60	60	15	Pass	50000	Pass
Pwrup Rise Time B=	63	usec	63	63	63	15	Pass	50000	Pass
Pwr Stagger Time SS4=	0	msec	0	0	0	-1	Pass	75	Pass
Pwr Stagger Time SS5=	0	msec	0	0	0	0	Pass	75	Pass
Pwr Stagger Time DS=	447.2	msec	447.2	447.2	447.2	0	Pass	1000	Pass
Test: pwrup_inrush									
Iinrush min Class 3=	430.5	mA	430.5	430.5	430.5	400	Pass	9999	Pass
Iinrush min Class 5=	867.5	mA	867.5	867.5	867.5	400	Pass	9999	Pass
Iinrush min Class 7=	869.8	mA	869.8	869.8	869.8	400	Pass	9999	Pass
Iinrush min Class 1D A=	427.9	mA	427.9	427.9	427.9	400	Pass	9999	Pass
Iinrush min Class 1D B=	428.1	mA	428.1	428.1	428.1	400	Pass	9999	Pass
Iinrush 4P max Class 3=	444.3	mA	444.3	444.3	444.3	0	Pass	450	Pass
Iinrush 4P max2 Class 5=	893.4	mA	893.4	893.4	893.4	0	Pass	900	Pass
Iinrush 4P max2 Class 7=	894.8	mA	894.8	894.8	894.8	0	Pass	900	Pass
Iinrush 2P max Class 3=	224.6	mA	224.6	224.6	224.6	0	Pass	450	Pass
Iinrush 2P max2 Class 7=	449.7	mA	449.7	449.7	449.7	0	Pass	600	Pass
Iinrush 2p max Cl 1D A=	437.6	mA	437.6	437.6	437.6	0	Pass	450	Pass
Iinrush 2p max Cl 1D B=	438.2	mA	438.2	438.2	438.2	0	Pass	450	Pass
Tinrush minPr Class 3=	62.31	msec	62.31	62.31	62.3	50	Pass	75	Pass
Tinrush maxPr Class 3=	62.31	msec	62.31	62.31	62.3	50	Pass	75	Pass
Tinrush minPr Class 7=	62.31	msec	62.31	62.31	62.3	50	Pass	75	Pass
Tinrush maxPr Class 7=	62.31	msec	62.31	62.31	62.3	50	Pass	75	Pass
Tinrush Class 1D A=	62.31	msec	62.31	62.31	62.3	50	Pass	75	Pass
Tinrush Class 1D B=	62.7	msec	62.7	62.7	62.7	50	Pass	75	Pass
Delay Inrush Class 7=	63.09	msec	63.09	63.09	63.1	50	Pass	75	Pass
Delay Inrush Class 2D A=	62.7	msec	62.7	62.7	62.7	50	Pass	75	Pass
Delay Inrush Class 2D B=	63.48	msec	63.48	63.48	63.5	50	Pass	75	Pass
45ms Pwr Stat Class 7=	1	****	1	1	1	1	Pass	1	Pass
45ms Pwr Stat Class 2D A=	1	****	1	1	1	1	Pass	1	Pass
45ms Pwr Stat Class 2D B=	1	****	1	1	1	1	Pass	1	Pass
Vinrush Class 2D A=	31.2	volts	31.2	31.2	31.2	30	Pass	60	Pass
Vinrush Class 2D B=	31.3	volts	31.3	31.3	31.3	30	Pass	60	Pass
Iinrush 16V DS3A=	316.25	mA	316.25	316.25	316.3	60	Pass	450	Pass
Iinrush 16V DS3B=	329.5	mA	329.5	329.5	329.5	60	Pass	450	Pass
Test: pwron_v									
Vpse Max Alt A=	54.83	V	54.83	54.83	54.83	50	Pass	57	Pass
Vpse Max Alt B=	54.8	V	54.8	54.8	54.8	50	Pass	57	Pass
Vpse Min Alt A=	54.2	V	54.2	54.2	54.2	50	Pass	57	Pass
Vpse Min Alt B=	54.05	V	54.05	54.05	54.05	50	Pass	57	Pass
Vport PSE diff=	10	mV	10	10	10	0	Pass	65	Pass
V ripple A=	19	mVp-p	19	19	19	0	Pass	500	Pass
V ripple B=	19	mVp-p	19	19	19	0	Pass	500	Pass
V noise A=	180	mVp-p	180	180	180	0	Pass	200	Pass
V noise B=	171	mVp-p	171	171	171	0	Pass	200	Pass
V trans A=	54.1	V	54.1	54.1	54.1	50	Pass	57	Pass
V trans B=	53.94	V	53.94	53.94	53.94	50	Pass	57	Pass
Test: pwron_pwrcap									
Max Asgn Class SS=	6	****	6	6	6	1	Pass	8	Pass

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PSA-3000 Ports

1-1

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P/F

Pcon_c1=	4.4	W	4.4	4.4	4.4	3.9	Pass	99.9	Pass
Icon_%c1=	112.4	%	112.4	112.4	112.4	100	Pass	125	Pass
Pcon_c2=	7.4	W	7.4	7.4	7.4	3.9	Pass	99.9	Pass
Icon_%c2=	112.4	%	112.4	112.4	112.4	100	Pass	125	Pass
Pcon_c3=	15	W	15	15	15	3.9	Pass	99.9	Pass
Icon_%c3=	112.4	%	112.4	112.4	112.4	100	Pass	125	Pass
Pcon_c4=	30.4	W	30.4	30.4	30.4	3.9	Pass	99.9	Pass
Icon_%c4=	112.4	%	112.4	112.4	112.4	100	Pass	125	Pass
Pcon_c5=	49.5	W	49.5	49.5	49.5	3.9	Pass	99.9	Pass
Icon_%c5=	112.1	%	112.1	112.1	112.1	100	Pass	125	Pass
Pcon_c6=	65.2	W	65.2	65.2	65.2	3.9	Pass	99.9	Pass
Icon_%c6=	111.9	%	111.9	111.9	111.9	100	Pass	125	Pass
Pcon_c7=	65.2	W	65.2	65.2	65.2	3.9	Pass	99.9	Pass
Icon_%c7=	111.9	%	111.9	111.9	111.9	100	Pass	125	Pass
Pcon_c8=	65.2	W	65.2	65.2	65.2	3.9	Pass	99.9	Pass
Icon_%c8=	111.9	%	111.9	111.9	111.9	100	Pass	125	Pass
Type_N_Enable=	1	****	1	1	1	1	Pass	1	Pass
Pclass_2p_c5=	-1	****	-1	-1	-1	1	Pass	1	Pass
Pclass_LLDP_95%=	1	****	1	1	1	1	Pass	1	Pass
Pclass_LLDP_75%=	1	****	1	1	1	1	Pass	1	Pass
Max_Asgn_Class_DS=	4	****	4	4	4	1	Pass	5	Pass
Pcon_c1DA=	4.4	W	4.4	4.4	4	3.9	Pass	49.9	Pass
Icon_%c1DA=	112.5	%	112.5	112.5	112.5	100	Pass	125	Pass
Pcon_c2DB=	7.5	W	7.5	7.5	8	3.9	Pass	49.9	Pass
Icon_%c2DB=	112.4	%	112.4	112.4	112.4	100	Pass	125	Pass
Pcon_c3DA=	15.5	W	15.5	15.5	16	3.9	Pass	49.9	Pass
Icon_%c3DA=	112.4	%	112.4	112.4	112.4	100	Pass	125	Pass
Pcon_c4DB=	32.6	W	32.6	32.6	33	3.9	Pass	49.9	Pass
Icon_%c4DB=	111.8	%	111.8	111.8	111.8	100	Pass	125	Pass
Pcon_c5DA=	32.6	W	32.6	32.6	33	3.9	Pass	49.9	Pass
Icon_%c5DA=	111.9	%	111.9	111.9	111.9	100	Pass	125	Pass
Test: pwron_unbal									
pseP2pUnbal_c4A=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c4B=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c5A=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c5B=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c6A=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c6B=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c7A=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c7B=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c8A=	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal_c8B=	1	****	1	1	1	1	Pass	1	Pass
pseP2pPkUnbal_A=	1	****	1	1	1	1	Pass	1	Pass
pseP2pPkUnbal_B=	1	****	1	1	1	1	Pass	1	Pass
Test: pwron_maxi									
Ilim_2p_max_SSA=	905.5	mA	905.5	905.5	905.5	0	Pass	1750	Pass
Ilim_2p_max_SSB=	904.8	mA	904.8	904.8	904.8	0	Pass	1750	Pass
Tlim_SS=	16.8	msec	16.8	16.8	16.8	10	Pass	75	Pass
Ilim_2p_max_DSA=	902.3	mA	902.3	902.3	902.3	0	Pass	1750	Pass

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Ilim_2p_max_DSB=	902.6	mA	902.6	902.6	902.6	0	Pass	1750	Pass
Tlim_DSA=	16.8	msec	16.8	16.8	16.8	10	Pass	75	Pass
Tlim_DSB=	16.8	msec	16.8	16.8	16.8	10	Pass	75	Pass
Ilim_min_cAB3=	400	mA	400	400	400	400	Pass	1750	Pass
Max_trans_c3=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB4=	684	mA	684	684	684	684	Pass	1750	Pass
Max_trans_c4=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB5=	580	mA	580	580	580	580	Pass	1750	Pass
Max_trans_c5=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB6=	720	mA	720	720	720	720	Pass	1750	Pass
Max_trans_c6=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB3D=	400	mA	400	400	400	400	Pass	1750	Pass
Max_trans_c3D=	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB4D=	684	mA	684	684	684	684	Pass	1750	Pass
Max_trans_c4D=	1	****	1	1	1	1	Pass	1	Pass
Vtrans_2p_A=	54.2	V	54.2	54.2	54.2	45.3	Pass	57	Pass
Vtrans_2p_B=	54.1	V	54.1	54.1	54.1	45.3	Pass	57	Pass
Iport_max_type3=	0	****	0	0	0	0	Pass	0	Pass
Test: pwrn_overld									
Ipeak_c1=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c2=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c3=	1	****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c3=	54.55	V	54.55	54.55	54.6	50	Pass	57	Pass
Ipeak_5%DC_c3=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c4=	1	****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c4=	54.33	V	54.33	54.33	54.3	50	Pass	57	Pass
Ipeak_5%DC_c4=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c5=	1	****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c5=	54.1	V	54.1	54.1	54.1	50	Pass	57	Pass
Ipeak_5%DC_c5=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c6=	1	****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c6=	53.85	V	53.85	53.85	53.9	50	Pass	57	Pass
Ipeak_5%DC_c6=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c1D=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c2D=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c3D=	1	****	1	1	1	1	Pass	1	Pass
Ipeak_c4D=	1	****	1	1	1	1	Pass	1	Pass
Test: mps_dc_valid									
Ihold_c3=	6	mA	6	6	6	4	Pass	9	Pass
Ihold_2p_c3A=	3	mA	3	3	3	2	Pass	5	Pass
Ihold_2p_c3B=	3	mA	3	3	3	2	Pass	5	Pass
Ihold_c5=	6	mA	6	6	6	4	Pass	14	Pass
Ihold_2p_c5A=	3	mA	3	3	3	2	Pass	7	Pass
Ihold_2p_c5B=	3	mA	3	3	3	2	Pass	7	Pass
Ihold_2p_c2DA=	3	mA	3	3	3	2	Pass	7	Pass
Ihold_2p_c2DB=	3	mA	3	3	3	2	Pass	7	Pass
LP_MPS_Tol_c3=	1	****	1	1	1	1	Pass	1	Pass
LP_MPS_Tol_c5=	1	****	1	1	1	1	Pass	1	Pass
LP_MPS_Tol_c2D=	1	****	1	1	1	1	Pass	1	Pass
Test: mps_dc_pwrn									
Tmpdo_c3A=	345.7	msec	345.7	345.7	345.7	320	Pass	400	Pass
Tmpdo_c3B=	345.7	msec	345.7	345.7	345.7	320	Pass	400	Pass
Tmpdo_c5A=	345.7	msec	345.7	345.7	345.7	320	Pass	400	Pass
Tmpdo_c5B=	345.7	msec	345.7	345.7	345.7	320	Pass	400	Pass
Tmpdo_c2DA=	345.7	msec	345.7	345.7	345.7	320	Pass	400	Pass
4pr_Stat_c2DA=	1	****	1	1	1	0	Pass	1	Pass
Tmpdo_c2DB=	345.7	msec	345.7	345.7	345.7	320	Pass	400	Pass
4pr_Stat_c2DB=	1	****	1	1	1	0	Pass	1	Pass
Test: pwrn_time									
Turnoff_time_Toff_A=	22.7	msec	22.7	22.7	22.7	0	Pass	500	Pass
Turnoff_time_Toff_B=	24	msec	24	24	24	0	Pass	500	Pass
Cout_A=	88.1	nF	88.1	88.1	88.1	0	Pass	520	Pass
Cout_B=	90.1	nF	90.1	90.1	90.1	0	Pass	520	Pass
Output_Rp_A=	119	Kohm	119	119	119	45	Pass	9999	Pass
Output_Rp_B=	124	Kohm	124	124	124	45	Pass	9999	Pass
Idle_time_SS=	535.2	msec	535.2	535.2	535.2	15	Pass	9999	Pass
Test: pwrn_v									
Error_Delay_SS_A=	1445	msec	1445	1445	1445	750	Pass	9999	Pass
Error_Delay_SS_B=	1445	msec	1445	1445	1445	750	Pass	9999	Pass
Error_Delay_DS_A=	1777	msec	1777	1777	1777	750	Pass	9999	Pass
Error_Delay_DS_B=	1523	msec	1523	1523	1523	750	Pass	9999	Pass

PSE Conformance Test Suite

September 29 2025	7:30 PM
Port Count.....	1
Loop Count.....	1
PSE Tested: 000.000.000.000 Type-3	



802.3bt 4Pr Conformance Report

version5.3.25

report version 5.3.07

Safety Index*:
Error Log: None

PSE Type: 3 MDI-X+MDI

100%

Interop Index*:
100%

Chassis ID: 192.168.221.105		PSA-3000 Ports								
TestLoop: 1		1-1	UNITS	Min	Max	Average	Low Limit	P/F	High Limit	P/F
Idle_Voff_SS_A=		0.9	V	0.9	0.9	0.9	0	Pass	2.8	Pass
Idle_Voff_SS_B=		0.9	V	0.9	0.9	0.9	0	Pass	2.8	Pass
Idle_Voff_DS_A=		0.8	V	0.8	0.8	0.8	0	Pass	2.8	Pass
Idle_Voff_DS_B=		0.8	V	0.8	0.8	0.8	0	Pass	2.8	Pass
Test Port Model Number:		3202								
Test Port Hardware Version:		11								
Test Port Firmware Version:		4.19 lc19								